

ABSTRACT

Disclosed is a digital signal processor with a unit of an integral number, on a basis of a characteristic distributing a sign of data composed of a predetermined unit from MSB (most significant bit) to LSB (least significant bit), changing a negative number to a positive number when the sign of data is the negative number and performing a logical OR operation in accordance with each sign of data so as to support a floating point operation through a sign bit, an efficient number and number of 0 of a final result.